

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

<b>In Re Application:</b>	)	<b>For:</b>	<b>CACHED MEMORY SYSTEM</b>
<b>Sih, et al.</b>	)		<b>AND CACHE CONTROLLER</b>
	)		<b>FOR EMBEDDED DIGITAL</b>
<b>Application No.: 10/807,648</b>	)		<b>SIGNAL PROCESSOR</b>
	)		
<b>Filed: March 24, 2004</b>	)	<b>Examiner:</b>	<b>Song, Jasmine</b>
	)		
<b>Confirmation No.: 3901</b>	)	<b>Group No.:</b>	<b>2188</b>
	)		
	)	<b>Docket No.:</b>	<b>030459</b>

Mailstop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY UNDER 37 C.F.R. § 1.116**

Sir:

In response to the Final Office Action of May 2, 2008, please consider the following:

**A Listing of the Claims** are reflected in the listing of claims which begins on page 2 of this paper; and

**Remarks/Arguments** begin on page 9 of this paper.